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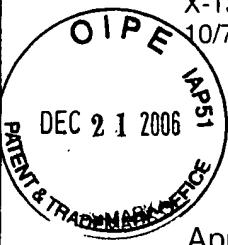
TRANSMITTAL FORM <i>(to be used for all correspondence after initial filing)</i>		Application / Conf. No.	10/717,040 / 7436
Mail Stop: AF		Filing Date	November 18, 2003
Express Mail Receipt No.		First Named Inventor	Tony K. Ngai
		Examiner Name	John J. Tabone Jr.
		Art Unit	2138
		Patent No.	
Total Number of Pages in This Submission		Attorney Docket Number	X-1363 US

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<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Assignment Papers (with Recordation Cover Sheet)	<input type="checkbox"/> After Allowance Communication to
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X-1363 US
10/717,040

PATENT
Conf. No. 7436

IN THE UNITED STATES PATENT OFFICE

Applicants: Tony K. Ngai et al.

Assignee: Xilinx, Inc.

Title: Self-Repairing Redundancy for Memory Blocks in Programmable Logic Devices

Serial No.: 10/717,040 File Date: 11/18/2003

Examiner: John J. Tabone Jr. Art Unit: 2138

Docket No.: X-1363 US Conf. No.: 7436

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AMENDMENT AND RESPONSE TO THE FINAL OFFICE ACTION

Dear Sir:

In response to the Final Office Action dated October 19, 2006, reconsideration and allowance of the application are respectfully requested in view of the amendment and remarks below.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 14 of this paper.